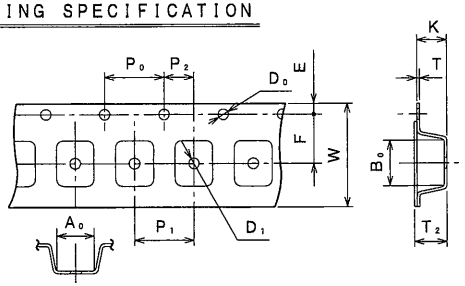
THIRD ANGLE PROJECTION

ITEM CODE	CAPACITANCE μF (*)	DIMENSIONS L	W	H	e1, e2	g	TYPE
ECPU 1C104MA5	0.10 (104)	2.0±0.2	1.25±0.2	1.0±0.2	0.45±0.25	0.6min	△2 J1
〃 1C154MA5	0.15 (154)	3.2±0.2	1.6 ±0.2	0.8±0.2	0.65±0.3	1.0min	H1
〃 1C224MA5	0.22 (224)	〃	〃	〃	〃	〃	〃
〃 1C334MA5	0.33 (334)	〃	〃	1.0±0.2	〃	〃	H2
〃 1C474MA5	0.47 (474)	〃	〃	1.4±0.2	〃	〃	H3
〃 1C684MA5	0.68 (684)	〃	〃	〃	〃	〃	〃
〃 1C105MA5	1.0 (105)	〃	2.5 ±0.2	〃	〃	〃	G2

TAPING SPECIFICATION



TYPE	A0	B0	T	T2	K
J1	1.55±0.1	2.3±0.1	0.25±0.05	1.3±0.2	1.2±0.1
H1, H2	1.9 ±0.1	3.5±0.1	〃	1.5±0.2	1.4±0.1
H3	〃	〃	〃	1.9±0.2	1.8±0.1
G2	2.8 ±0.1	〃	〃	〃	〃

SYMBOL	DIMENSIONS
W	8.0 ±0.3
F	3.5 ±0.05
E	1.75±0.1
P1	4.0 ±0.1
P2	2.0 ±0.05
P0	4.0 ±0.1
φD0	1.5 +0.1 0
φD1	1.0 +0.2 0

ALTERATION

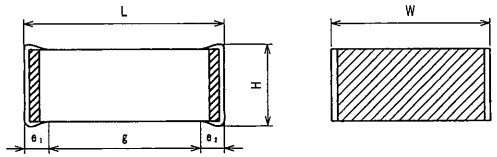
ISSUE	DESCRIPTION	DATE
△2	Alteration (Type J2→J1)	Aug. 24 2002
△3	Alteration Packing specification W1 (9.0+0.3→9.0+1.0) Company name changed	Oct. 1 2004
△4	Company name changed	Apr. 1 2005
△5	Company name changed	Apr. 1 2006
△6	Company name changed	Apr. 1 2008
△7	Company name changed	Apr. 1 2012
△8	Company name changed	Apr. 1 2013
△9	Company name changed Error correction:Withstand voltage	Apr. 1 2015

SPECIFICATIONS No.

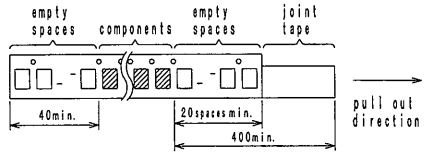
ITEM CODE NUMBER STRUCTURE

ECPU1C105MA5 (DC16V, 1.0μF, ±20%)

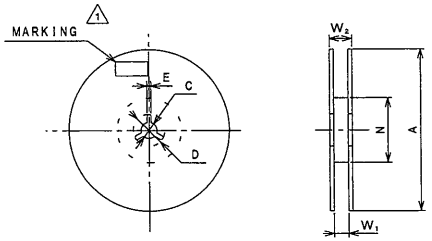
- Taping code
- Suffix
- Capacitance tolerance
- Capacitance : See table (*)
- Rated voltage (1C=DC16V)



EMPTY SPACE AND JOINT TAPE



PACKING SPECIFICATION



SYMBOL	DIMENSIONS
A	180.0 -0 -1.5
C	13.0 ±0.2
D	21.0 ±0.8
E	2.0 ±0.5
N	60.0 +1.0 0
W1	△3 9.0 +1.0 0
W2	11.4 ±1.0

PACKING QUANTITY

TYPE	REEL (pcs.)
J1, H1, H2	3,000
H3, G2	2,000

Reference

CONSTRUCTION

The capacitor is non-inductive construction, stacked with metallized plastic film dielectric, and has two outer electrodes.

PROPERTIES

Capacitance	: See table at 1kHz	
Capacitance tolerance	: ±20% (M)	
Rated voltage	: DC16V	
Withstand voltage	: △9 Rated voltage × 150% for 60s	
Insulation resistance	: I. R≧1,000MΩ when C≦0.33μF	at 20℃, DC16V, for 60s
	: C. R≧300MΩ·μF when C>0.33μF	
Dissipation factor	: ≦1.5% at 1kHz, 20℃	
Category temperature range	: from −40℃ to +85℃ (including temperature rise on unit surface)	

DESIGN	M. Nakamura
CHECKED	M. Yanagimoto
APPROVAL	Y. Takata
ESTABLISHMENT	Aug. 1. 1998
TYPE NAME	ECPU 1C***MA5
NAME	FILM CHIP CAPACITOR
DRAWING NAME	PRODUCT DRAWING
DRAWING No.	CW-H-803H (1/1)

Toyama·Matsue Plant
Device Solutions Business Division
Panasonic Corporation

DO NOT SCALE DRAWING REVISIONS INDICATED BY △ ALL DIMENSIONS ARE IN MILLIMETERS